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Title of Application	METHOD OF GENERATING A CHAOS- BASED PSEUDO-RANDOM SEQUENCE AND A HARDWARE GENERATOR OF CHAOS-BASED PSEUDO RANDOM BIT SEQUENCES
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Assignee	STMicroelectronics S.r.l.
Group Art Unit	2193
Examiner	Ngo D. Chuong
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## **CERTIFICATE OF MAILING OR TRANSMISSION**

I hereby certify that this correspondence is being transmitted via the Office electronic filing system, EFS-Web, addressed to: MS Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 12<sup>th</sup> day of August, 2008.

<u>/Paola Kuvac/</u>	
Signature	